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APPENDIX

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APPENDIX A

Derivation of Equation (3.1)

There are c microprocessors in the multiple-microprocessor system. Assuming that the data transaction arrival rate is distributed according to Poisson distribution with mean λ and the processing time for each microprocessor is exponential distribution with mean μ_i , $i=1,\,2,\,3,\ldots,c$.

Considering the mean service rate of this system, if there are more than c transactions in the system, all the c microprocessors are busy and each is processing at a mean of μ_1 , $i=1, 2, \ldots, c$ and the mean system output rate is thus $\mu_1 + \mu_2 + \ldots + \mu_c$. When they are fewer than c transactions in the system, only n of c servers are busy and the mean service rate of the system can be derived as follows.

n=1: upon entry into the system, the transaction will be proceeded to any microprocessor with equal probability. It will then speed an exponentially distributed interval of time in microprocessor i whose mean is $1/\mu_i$. After that interval the transaction departs and only then a new transaction is allowed to enter the processing facility.

It is clear from the description that the processing time p.d.f. will be given by

$$b(x) = \frac{1}{c}\mu_{1}e + \frac{1}{c}\mu_{2}e + \cdots + \frac{1}{c}\mu_{c}e^{-\mu_{c}x}$$

x = microprocessor processing time Taking the Lapace transform, and set s = 0, we get the expectation of processing time \overline{x}

$$\bar{x} = \frac{1}{c} \begin{bmatrix} c \\ i = 1 \end{bmatrix} \bar{\mu}_i$$

The mean processing rate becomes

$$\mu = 1 / \frac{1}{c} \left[\sum_{i=1}^{c} \frac{1}{\mu_i} \right]$$

$$\mu = \frac{c}{c}$$

$$i = 1 \quad \frac{1}{\mu_i}$$

The flow balance equation is as follows:

n=1:
$$\frac{c}{c}$$

$$i = 1 \begin{pmatrix} \frac{c}{\mu_i} \end{pmatrix}$$

$$p_1 = \frac{\lambda p_0}{\lambda p_0}$$

$$p_1 = \frac{\lambda p_0}{\lambda p_0}$$

$$i = \frac{\lambda p_0}{\lambda p_0}$$

$$i = \frac{\lambda p_0}{\lambda p_0}$$

$$p_1 = \delta p_0$$
 where $\delta = \lambda / \frac{c}{c}$
 $i = \frac{\Sigma}{i} \frac{1}{\mu}$

n = c

n = c+1

$$P_{n} = \begin{cases} \frac{1}{n!} \delta^{n} P_{0} & 1 < n < c \\ \frac{\delta^{c-1} \rho^{n-(c-1)}}{(c-1)!} P_{0} & c \le n \end{cases}$$
(3.1)

AUTOBIOGRAPHY

Mr. Borworn Papasratorn was born in Bangkok on July 28, 1957.

He received M.Eng (EE) in Communication Engineering from the

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During 1980 and 1985, he was with the Department of Aviation, Thailand, as an electronic engineer. His responsibility included design, installation and maintenance of the air navigation aided equipments. At present, he is a lecturer in the Department of Computer Engineering, King Mongkut's Institute of Technology Thonburi.

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